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Logics for Mazurkiewicz Traces

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Logics for Mazurkiewicz Traces

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Abstract

Linear temporal logic (LTL) has become a well established tool for specifying the dynamic behavior of reactive systems with an interleaving semantics and the automata-theoretic approach has proven to be a very useful mechanism for performing automatic verification in this setting. Especially alternating automata turned out to be a powerful tool in constructing efficient yet simple to understand decision procedures and directly yield *on-the-fly* model checking procedures.

While this technique extends elegantly to richer domains where the underlying computations are modeled as (Mazurkiewicz) traces, it does so only for event- and location-based temporal logics. In this thesis, we exhibit a decision procedure for LTL over Mazurkiewicz traces which generalizes the classical automata-theoretic approach to a linear temporal logic interpreted no longer over sequences but restricted labeled partial orders. Specifically, we construct a (linear) alternating Büchi automaton accepting the set of linearizations of those traces satisfying the formula at hand. The salient point of our technique is to apply a notion of independence-rewriting to formulas of the logic. Furthermore, we show that the class of *linear* and *trace-consistent* alternating Büchi automata corresponds exactly to LTL formulas over Mazurkiewicz traces, lifting a similar result from Löding and Thomas formulated in the framework of LTL over words.

Additionally, a linear temporal logic with a different flavor is introduced as *Foata linear temporal logic* ($\mathrm{LTL_f}$). It is designed for specifying properties of *synchronized* systems that comprise *clocked* hardware circuits or *Petri nets* supplied with a *maximal step semantics.*

Distributed synchronous transition systems (DSTSs) are introduced as formal models of these systems and are equipped with a *Foata configuration graph*-based semantics, which provides a link between these systems and the framework of Mazurkiewicz traces. To simplify the task of defining DSTSs, we introduce a simple calculus in the spirit of CCS.

We give optimal decision procedures for satisfiability of $\mathrm{LTL_f}$ formulas as well as for model checking, both based on alternating Büchi automata. The model checking procedure further employs an optimization which is similar to a technique known as *partial order reduction.*

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Contents

List of Figures

Chapter 1

Introduction

Electronic devices are nowadays ubiquitous in our society. We are in touch with hardware and software systems when using the telephone, the Internet, or the cash dispenser (automated teller machine, ATM). But also typical mechanical devices like the washing machine or the car are meanwhile full of small electronic systems that optimize the washing program or the control of the anti-skid system.

Reliability is one of the crucial issues going along with the development of these electronic devices. Faults can result in enormous loss of money, when, for example, a spacecraft like the Mars Pathfinder is lost because of a communication problem, or a bug in a digital circuit like the Intel® Pentium™ is encountered when already hundred thousands of processors are on the market. But also grave problems can arise when a telephone system for a whole region or an air traffic control system fails.

System validation, i.e., the process of determining the correctness of specifications, designs, and products, is thus an increasingly important activity [Kat99]. Current practice in conventional software engineering is to check a system to a large extent by humans (so-called "peer reviewing") and by dynamic testing. A further technique to achieve reliable systems is the application of appropriate design methodologies [GHJV00]. Even when coping with many rules of good design and even when testing is applied, the designed system may contain errors. One reason is that programmers are tempted to find errors they are already aware of. It is thus crucial to use automatic procedures and tools that attempt to remove man-made mistakes from system realizations. This is the goal of *formal methods*.

Formal methods The term *formal methods* denotes the application of mathematical methods for modeling, specifying, and verifying complex hardware and software systems. Especially distributed systems, which are much more difficult to understand due to their ability to execute actions concurrently, are developed using formal

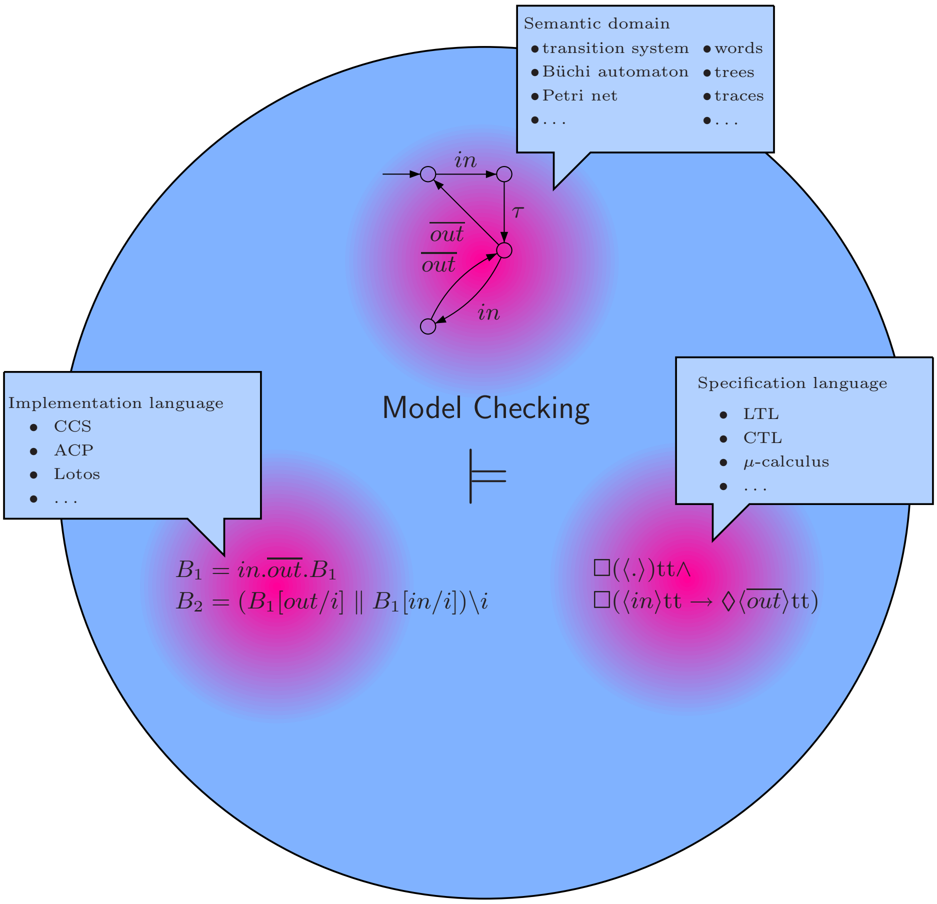

Figure 1.1: The idea of model checking

methods. Confer [BH99] for examples showing the benefits of formal methods in practice.

The process of developing a system usually starts with formulating its *specification*. This step consists of collecting a list of requirements the system-to-be should meet. The requirements are often written down in some logical formalism like monadic second-order logic [HJJ$^+$95, ABP97], linear temporal logic (LTL, [Pnu77]), computation-tree logic (CTL, [CE81]), or the μ-calculus [Koz83]. This part is depicted in right lower corner of the picture shown in Figure 1.1.

A complementary step is to define a formal *model* or *implementation* of the system, which helps to understand the system under development. Furthermore, a common and formal basis for discussing about the system is given. Last but not least, it is the basis of the realization of the system.[1] Usually, a kind of process-algebra formalism

[1] We speak of *implementation* although we actually might deal with an abstraction of a real

like CCS [Mil89], ACP [BV94], LOTOS [BB89], or another (semi-)formal design notion like $B(PN)^2$ [BH93], VHDL [Per91], or UML [SP99] is employed. This step is shown in the lower left corner of Figure 1.1.

The *verification* of the implemented system is a further step. Its aim is to guarantee the correctness of the functionality. In practice, verification is often more important for debugging the design instead of showing that the design is correct. This implies that verification usually proceeds in a cycle of finding errors and correcting the implementation until no further errors can be detected.

However, to be able to say that a given implementation expressed in some formal language satisfies its specification expressed in some probably different formal language, there has to be some common semantic domain to compare both sides (cf. Figure 1.1 on the preceding page). Usually, the implementation is transformed into some finite state model. Within the linear temporal framework, a formula expresses a property of a sequence of actions of the underlying system, so that we can take all sequences of executions of the finite state model to decide whether the formulated requirement holds. In the setting of branching time, a formula expresses a property of the computation tree of the underlying system. Here we can "unwind" the transition system to get an answer.

Especially for concurrent systems, the question of the "right" semantic domain is difficult to answer. Several proposals have been given and were analyzed in the past. Transition systems, Petri nets, Mazurkiewicz traces, and event structures head the list. In [SNW96], several models are compared on a formal basis.

The common idea of all models is that each is based on atomic units that are indivisible and constitute the steps from which computations are built. They differ in the level of abstraction from the underlying system. For example, so-called *interleaving models* reduce concurrency to non-determinism. It is postulated that the concurrent execution of two actions a and b is equivalent to the choice of executing a and then b or b and then a, abstracting from *true* concurrency in this way.

Within the true-concurrency approach, the execution of a concurrent system is described by means of partial order retaining the notion of concurrent or independent actions. In other words, the computations of a distributed system will be constituted by interleavings of the occurrences of causally independent actions that can be naturally grouped together into equivalence classes where two computations are equated in case they are two different interleavings of the same partially ordered stretch of behavior.

This observation led to the notion of Mazurkiewicz traces [DM97, DR95]. Traces can be understood as partially commutative words that model the behavior of a concurrent system. While the study of partially-commutative monoids traces back to

implementation. For example, we also call a protocol definition in some formal language an implementation rather than only its implementation in some low level programming language.

Cartier and Foata [CF69], it was Mazurkiewicz [Maz77] who applied these structures for modeling concurrent systems. The exciting point on Mazurkiewicz traces is that only a single representative for each equivalence class might be employed for verifying a desired property. This is the insight underlying many of the partial-order based verification methods (e.g. [Pel98, Val91]). As may be guessed, the importance of these methods lies in the fact that the computational resources required for the verification task can often be dramatically reduced.

In general, two approaches for the verification of systems can be distinguished: *model checking* and *theorem proving* [RV01]. Several case studies have shown that especially model checking admits to find errors during the design process (cf. [CW96] for an overview). In this thesis we focus on model checking.

Model checking The crucial virtue of model checking is that it proceeds automatically. The implementation as well as the specification is given to a computer tool which clarifies on its own whether the implementation satisfies its specification. No user interaction is needed for this purpose. Additionally, if an error is recognized, the tool provides a counter example showing under which circumstances the error can be generated. Model checking originates from the independent work of Emerson and Clarke [EC82] and Quielle and Sifakis [QS82]. Several prototypes of model checking tools like the Edinburgh Concurrency Workbench [Mol92], SPIN [GHP97], the symbolic model checker SMV [McM92], and Truth [LN01] have been developed and are used to demonstrate the benefits of this approach.

Model checking is especially suited when the implementation is given by (or can be translated into) a finite state model $\mathcal{M}$ and the specification is given in form of a temporal-logic formula φ; the automata theoretic approach has turned out to be fruitful here. The implementation is transformed into a corresponding automaton $\mathcal{A}_{\mathcal{M}}$ accepting all behaviors of the system. The specification is negated and an automaton $\mathcal{A}_{\neg\varphi}$ is constructed accepting all models of $\neg\varphi$. Next, the intersection automaton $\mathcal{A} = \mathcal{A}_{\mathcal{M}} \cap \mathcal{A}_{\neg\varphi}$ of $\mathcal{A}_{\mathcal{M}}$ and $\mathcal{A}_{\neg\varphi}$ is built. The language accepted by $\mathcal{A}$ consists of all counter examples of $\mathcal{M}$ violating φ, so that $\mathcal{A}$ can first be checked for emptiness to answer whether the implementation satisfies its specification and second be consulted to present a counter example. Note that $\mathcal{A}_{\varphi}$ can be tested for emptiness whether φ is satisfiable. Thus, this approach incorporates a decision procedure for satisfiability of the specification, answering the question whether the specification is contradictory.

The automata-theoretic approach for satisfiability checking was introduced in the pioneering work of Büchi [Büc60] for monadic second-order logic over words. It was transferred to the domain of temporal logics by Vardi and Wolper [VW86]. It has proven to be very useful and efficient for performing the automatic program verification.

In the last years, the general interest shifted towards alternating automata. They provide means for simple, efficient, and easy to understand decision procedures. Satisfiability algorithms for LTL over words [Var96], branching time logics [BVW94, KVW00] over finite transition systems, and Kozen's μ-calculus [Koz83] over (infinite) pushdown and prefix-recognizable graphs [KV00][2] have been defined using this variant. The idea is that the states of the automaton are constructed essentially from the subformula closure of the specification formula, and the automaton operates in a tableau-like fashion. The satisfiability problem is then solved by checking whether the constructed automaton accepts any words.

The automata-theoretic approach forms the conceptual basis of many verification algorithms. Several tools (e.g. SPIN [GHP97]) being employed in industry are built upon this translation from formulas to automata. To improve performance, however, a number of substantial optimizations must be incorporated. One observation is that the state space of the product automaton needs seldomly to be fully constructed. Often the answer to the verification problem can be established by investigating only a subset of states, and this subset might be considerably smaller than the entire state space. This is the main idea underlying the so-called *on-the-fly* verification techniques. To support on-the-fly checking, an automaton corresponding to a formula should be defined in a *top-down* manner, hereby allowing a part of the automaton to be analyzed while a different part is yet not constructed. This offers the possibility that the automaton for a given formula as well as a finite state system is only partly constructed viz when an initial segment already clarifies that the specification is fulfilled, or, more often in practice, contains already a counter example.

Temporal logics for traces Linear temporal logic (LTL) as proposed by Pnueli [Pnu77] has become a well established tool for specifying the dynamic behavior of distributed systems. The traditional approach towards automatic program verification is model checking specifications in LTL. A basic feature of LTL has been that its formulas are interpreted over sequences. Typically, such a sequence will model a computation of a system: a sequence of states visited by the system or a sequence of actions executed by the system during the course of the computation.

Indeed, it is often the case that the computations generated by a distributed system constitute Mazurkiewicz traces. It likewise turns out that many of the properties expressed as LTL formulas happen to have the so-called "all-or-none" property. Either all members of an equivalence class of computations will have the desired property or none will do ("leads to deadlock" is one such property). For verifying such properties one has to check them for just one member of each equivalence class.

[2] Overviews of prefix-recognizable graphs and their monadic-second order theory can be found in [Cau96] and [Leu02]

However, it is a simple matter to specify properties within LTL for words that are not so-called *trace consistent.* From a practical point of view, it is not convenient to allow a prospective user of a verification tool to formulate such requirements. The user should be guided to specify only requirements respecting the inherent structure of an underlying system.

This problem can be tackled by developing linear temporal logics that can be interpreted directly over Mazurkiewicz traces. In these logics, every specification is guaranteed to have the "all-or-none" property and hence can be subjected to the partial-order based reduction methods during the verification process.

A number of linear temporal logics to be interpreted directly over Mazurkiewicz traces (e.g. [APP95, Thi94, TW97]) has been proposed starting with TrPTL [Thi94] that was introduced by Thiagarajan (see [MT96, TH98] for overviews). There are several possible routes towards extending linear temporal logics to traces. TrPTL is based on *locations*, where one reasons explicitly about a distribution of computing agents cooperating through some communication structure given as an alphabet distribution. Another option [APP95] is to view *events* as the partial order computation points in time and base the specifications upon the relationship between individual events. Together, these paradigms constitute the *local* trace logics. In contrast, in the *global* view of computations, *configurations* are seen as instantaneous snapshots of the system at hand. In this sense, a configuration is a global view capturing a collection of simultaneous local views.

The "right" temporal logic for traces should be equal in expressive power to first-order logic for traces (FO). It follows from [EM96] that such a logic would capture exactly those properties of LTL which have the "all-or-none" property and hence are amenable to partial-order verification. However, none of the local logics is known to be expressively equivalent to FO. Recently, it was indeed shown that linear temporal logic (without past tense modalities) is expressively equivalent to FO iff the independence adheres to a certain structure [DG01]. This lead Thiagarajan and Walukiewicz to define the configuration based LTrL [TW97], that they indeed prove equivalent to FO. LTrL was later refined by Diekert and Gastin [DG00] to a straightforward formulation of LTL for traces essentially extending Kamp's Theorem [Kam68] to the setting of traces.

While both the event based and location based logics all have elegant (exponential-time) decision procedures smoothly extending the classical automata-theoretic approach to the setting of traces, no such smooth extension exists for global logics such as LTL. The essence of this anomaly is the complications which arise as a consequence of the fact that the satisfiability problem for LTL has a non-elementary lower bound [Wal98]. However, experience [HJJ$^+$95] has shown that decision procedures can still be useful in practice despite discouraging lower bounds.

Gastin, Meyer, and Petit [GMP98a, GMP98b] do give a direct decision procedure for

LTL based on automata. However, the construction of the automaton corresponding to a given LTL specification φ proceeds by induction on φ, thus in a *bottom-up* manner. Hence, it is not an extension of the classical automata-theoretic approach, and, more importantly, it requires the construction of the full automaton, so optimizations such as on-the-fly checking cannot be applied. A further drawback is its high complexity. While an exponential blow-up is unavoidable for nested *until-formulas*, the procedure has also an exponential blow-up for every negation. Since nested *until*-formulas are rare in specifications but negations are typical for specifying unwanted behavior, this limits the practical applicability of this procedure.

In this thesis, we propose a decision procedure for LTL for traces directly extending the classical approach [Var96]. Our procedure is based on an extended subformula closure and independence rewriting of formulas of LTL. We employ this to construct a tableau-style alternating Büchi automaton accepting the set of linearizations of traces satisfying the specification at hand. In this sense, our procedure fills the missing gap for global trace logics by extending the classical approach to this remaining case. Our procedure corresponds exactly to the version introduced by Vardi [Var96] when restricted to the word case. Thus, we provide a *conservative extension* of the traditional approach, simplifying the theoretical understanding while offering a single yet efficient implementation for words as well as for traces. Furthermore, our automata can be constructed on-the-fly, which speeds up the average time and space used for checking specifications.

We furthermore present a simplified decision procedure for the case that the *until*-operator is substituted by an *eventually*-operator. We show that our decision procedure meets the known lower bound for a restricted kind of alphabets, so that it is optimal in these cases. Last but not least, for the fragment of LTL without *until*-operator and *eventually*-operator, our procedure is shown to be exponential.

While our approach is primarily used in this thesis to show how to cope with LTL for Mazurkiewicz traces, it should be noted that it might be a general technique applicable for temporal logics when partial commutation is present. For example, our method was used in [BL01b] to present a decision procedure for satisfiability of a linear temporal logic for message sequence charts.

Löding and Thomas [LT00] have shown that word languages definable by LTL formulas over words correspond to the languages of *linear* alternating Büchi automata. We prove that our construction yields a linear Büchi automaton as well. Furthermore, we show that our linear Büchi automata accept *trace-consistent* languages. Conversely, we show that the class of trace-consistent languages definable by linear alternating Büchi automata coincides with the class of languages which are definable by LTL formulas over Mazurkiewicz traces for a given dependency relation. In other words, LTL-definable trace languages correspond to languages definable by trace-consistent linear alternating Büchi automata.

Some aspects of the obtained results have been presented in [BL01a].

A linear temporal logic with a different flavor is introduced as *Foata linear temporal logic* ($\mathrm{LTL_f}$). It is designed for specifying properties of *synchronized* systems. Important examples among these systems are hardware circuits that are built up by separate entities working together in parallel but which are synchronized by a global clock. Another model are *Petri nets* supplied with a *maximal step semantics* [Muk92]. The approach was partly introduced in [Leu00].

The idea underlying this model is somehow a reinforcement of the true concurrency approach. If two actions a and b are supposed to occur concurrently, we consider a concurrent system only in the configuration where no action has occurred as well as in the configuration where both actions have occurred. We refrain from considering configurations in which only one of the actions has been noticed since these might be used to differ the interleavings ab and ba.

We exhibit the notion of *distributed synchronous transition systems* (DSTSs) as a model for these hardware designs. DSTSs can be equipped naturally with a *Foata configuration graph*-based semantics which provides a link between these systems and the framework of Mazurkiewicz traces.

We give a decision procedure for satisfiability of $\mathrm{LTL_f}$ formulas as well as a model checking procedure, both based on alternating Büchi automata. It turns out that these procedures are as efficient as for LTL (for words) viz they are exponential in the length of the formula and linear in the size of the system and are essentially optimal. The model checking procedure employs an optimization which is similar to *partial order reduction* [Pel98].

To simplify the task of defining DSTSs, we introduce a simple calculus, which we call *synchronous process systems* (SPS) and which is inspired by Milner's CCS [Mil89] and SCCS [Mil83] but is adapted to the special nature of our underlying systems.

Outline of this thesis In the next chapter, we describe a typical scenario of a distributed system. Here we will spot the characteristics of such a system. This will guide us to define the formal notion of Mazurkiewicz traces.

In Chapter 3, we introduce Mazurkiewicz traces. We start with defining alphabets together with an independence relation. Subsequently, we will derive the notion of a trace and configurations of a trace, which serve as models for the temporal logics studied. Furthermore, we provide a link with the theory of words. This allows us to employ the rich theory developed in this domain (especially automata theory) for partial orders.

Chapter 4 recalls the notions of (alternating) Büchi automata. We identify especially *linear* and *trace-consistent* alternating Büchi automata.

To be able to formulate key results for linear temporal logics, we recall first-order logic for words as well as for traces in Chapter 5.

Chapter 7 exhibits one of the main contributions of this dissertation. We present herein the linear temporal logic LTL, which is interpreted over Mazurkiewicz traces, and give a decision procedure for checking satisfiability of LTL formulas. To simplify the overall presentation, we recall a decision procedure for LTL over words in Chapter 6, which gives the outline also in the setting of traces. We first present our procedure for the Hennessy-Milner fragment of LTL in Chapter 7.2.1 and extend this procedure to full LTL in Chapter 7.2.2.

As already mentioned, deciding satisfiability of LTL for traces is non-elementary. LTL^- is introduced as a fragment of LTL whose formulas can be checked for satisfiability in exponential space. We adapt our decision procedure for this fragment in Chapter 7.4.

So-called *clocked* hardware systems are studied in Chapter 8, and this chapter is another main ingredient of this thesis. We describe how to model these hardware systems as *distributed transition systems*, that, on their part, constitute traces as their executions. We introduce a simple calculus simplifying the definition of these systems. Furthermore, we define a linear temporal logic, called *Foata LTL*, that is well-suited to formulate specifications of these systems. Optimal procedures for checking satisfiability as well as for model checking are presented. To round off this framework, we end this chapter with a larger example explaining this model.

We sum up our main results in Chapter 9.

Chapter 2

Motivation

Concurrent systems play an important rôle in computer science but also in many different engineering disciplines. However, the last statement induces the fundamental question:

> "What is a concurrent system?"

We do not intend to give a formal definition. Instead we will come up with examples describing our notion of concurrent systems. Hereby, we will derive a set of parameters characterizing concurrent systems. These will guide us to develop the framework of Mazurkiewicz traces, which then captures formally the kind of concurrent systems that we will be able to handle in this thesis.

Let us consider the setting consisting of a bank, an ATM, and a customer (cf. Figure 2.1) and the well-known task of withdrawing money. When a customer tries to withdraw money from his bank account using the ATM, the machine checks whether the requested amount of money is available on the customer's account. If so, the ATM will offer the money to the customer and the withdrawal is acknowledged to the bank. Otherwise, a rejection of the customer's request is displayed to him.

What kind of ingredients can be identified in this example? We have three entities: the bank, the ATM, and the customer. The entities operate autonomously and we call them *processes*. Every process issues some *actions*. For example, the ATM

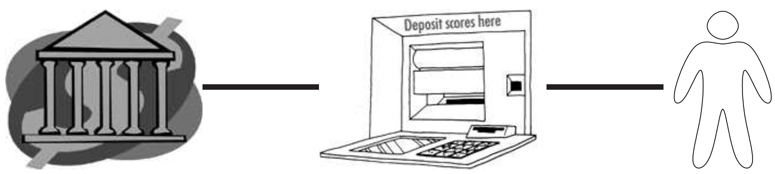

Figure 2.1: A concurrent system

generates print actions which can be read by the customer. The customer, on the other hand, communicates with the ATM. He or she enters, for example, the bank card, which is then collected by the machine. Thus, we can *observe* a *communication action* viz that one representing the collection of the card. A further action that may be observed is the lookup of the customer's bank account. Of course, a lookup action can only be observed if the bank card has been taken by the ATM. Both actions are therefore *dependent*.

Taking a closer look to the bank, the acknowledgement of the withdrawal of the money might result in an *internal* action involving a database located inside the bank. Of course, the database action is not *causally dependent* on the customer's further activities. He or she might take back his or her bank card while the database action takes place. Both actions are *independent*.

The described scenario might be modeled formally by three state transition systems for the bank, the cash dispenser, and the customer, respectively. The transitions of each system are labeled using a finite set of actions representing the internal and communication actions each process is capable of executing. Some of the actions are naturally dependent while others are independent. Observing a single execution of such a system, the adequate structure is a partial order instead of a linear order because there is no reason to distinguish the cases where the database action happens before or after taking back the bank card. It makes a difference though whether the requested money is granted or not. Each transition system may therefore be non-deterministic, if several alternatives are abstracted by non-determinism. Altogether, the overall system may be described by the set of all executions, which is then a set of partial orders.

Often, it is reasonable to abstract from internal actions and to concentrate on communication actions. Analyzing distributed systems, it is especially the communication that involves difficulties. Internal actions of each process are carried out sequentially so that conventional debugging techniques can be employed. Milner's CCS [Mil89] incorporates this view. Note however, that this view is not always adequate, as we will see in Chapter 8.

In the previous example, all communication actions are dependent. The ATM ensures that the communication actions of the bank and the ATM on one hand as well as the ones of the ATM and the customer on the other are dependent.

If we take a more complicated setting, we also obtain independent actions even when restricting to communication actions. Take for example the schematic view of the airplane, as shown in Figure 2.2 on the facing page. The airplane has several fuel tanks. Within each tank, several fuel probes are installed for measuring the fuel level. Each tank is equipped with a separate (distributed) flight data acquisition unit, which checks the fuel probes and communicates with other units. Thus, we have for each tank a system consisting of fuel probes and data acquisition unit, and

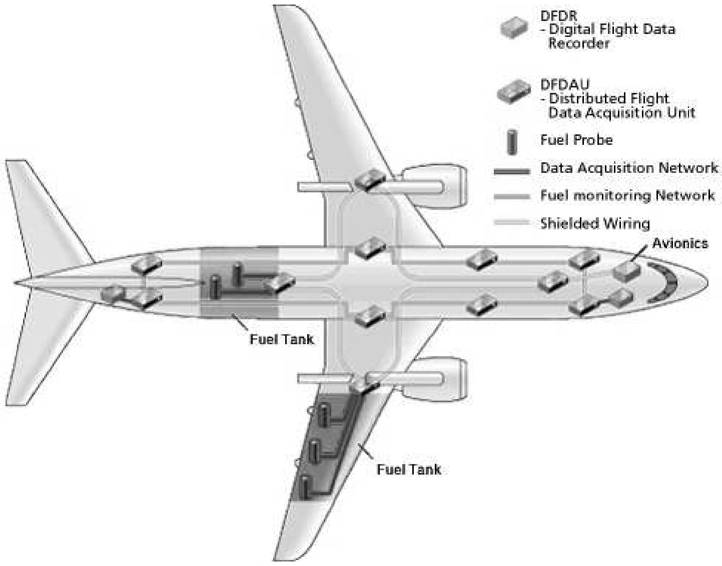

Figure 2.2: An airplane

these systems communicate independently inside.

We have now set out the scene of the systems we want to model and to study. We will meet this goal in the following chapters.

Chapter 3

Mazurkiewicz Traces

In this chapter, we define and study the semantic domain which may be employed for describing concurrent systems. We start with considering *alphabets*, which contain the atomic entities for describing atomic *actions* of our systems. *Mazurkiewicz traces*, for the sake of brevity usually just called *traces*, correspond to sequences of such atomic actions and are employed to model *executions* of our systems. Concurrent systems then may be described by sets of executions, called *languages* in this framework. Partial executions give rise to the notion of *configurations*, and executions may be analyzed with respect to their configurations.

3.1 Alphabets

We directly start with the elementary and simple notion of an alphabet.

Definition 3.1.1
An alphabet *is a non-empty finite set. The elements of* Σ *are called* actions*.*

We usually denote alphabets by Σ or Σ' and actions by $a, b, c, \ldots$ and $a', b', c', \ldots$, respectively. The elements of an alphabet, actions, are the atomic entities which may be executed by our concurrent system. As pointed out in the previous chapter, these actions can be equipped with a natural notion of *dependence* and dually also *independence*.

Definition 3.1.2
A symmetric and irreflexive relation $I \subseteq \Sigma \times \Sigma$ *is called an* independence relation *over* Σ*, and* $D := \Sigma^2 \backslash I$ *is called its* dependence relation*. The pair* (Σ, I) *is called an* independence alphabet*, and, similarly,* (Σ, D) *is called a* dependence alphabet*, respectively.*

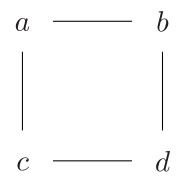

Figure 3.1: A dependence alphabet

Sometimes, we will refer to $\mathcal{I} = (\Sigma, I)$ by the name *concurrent alphabet* ([Maz88]). Let Σ be an alphabet. Then $(\Sigma, \emptyset)$ is an independence alphabet whose actions are all pairwise dependent. We call this alphabet also the *fully-dependent* alphabet. On the other hand, $(\Sigma, (\Sigma \times \Sigma) \setminus \Delta(\Sigma))$ is an independence alphabet in which all actions are independent (except each action on itself).[1] This alphabet is also called the *fully-independent* alphabet. A *transitive* dependence alphabet is a dependence alphabet (Σ, D) such that D is a transitive relation.

An independence alphabet (Σ, I) can be visualized as a graph[2] where the nodes correspond to the actions in Σ and two nodes a and b are connected iff $(a, b) \notin I$ and $a \neq b$. Thus, whenever two distinct actions are dependent they are connected by an edge. For simplicity we omit loops. In the same manner, a dependence alphabet (Σ, D) will be represented graphically by the graphical representation of the independence alphabet (Σ, I) where $I = \Sigma^2 \setminus D$. Let us fix the previous considerations in the following definition.

Definition 3.1.3
Let (Σ, D) be a dependence alphabet. The dependence graph *of (Σ, D), denoted by $\mathcal{G}(\Sigma, D)$, is the graph (V, E) where $V = \Sigma$ and*

$$E = \{(a, b) \in \Sigma \mid (a, b) \in D \text{ and } a \neq b\}$$

The dependence graph $\mathcal{G}(\Sigma, I)$ of an independence alphabet is defined as described before.

Example 3.1.4 Let us consider the independence alphabet (Σ, I) with a set of actions $\Sigma = \{a, b, c, d\}$ and the independence relation $I = \{(a, d), (d, a), (b, c), (c, b)\}$. The dependence graph $\mathcal{G}(\Sigma, I)$ is isomorphic to the one shown in Figure 3.1.

[1] $\Delta(R)$ is defined by $\{(q, q) \mid q \in R\}$.

[2] We use notions and notations from graph theory as usual. If necessary, please consult Appendix A.2 on page 153 for details.

Observe that the dependence graph of a fully-independent alphabet has no edges and that the one of a fully-dependent alphabet is a complete graph.[3] Given a transitive dependence alphabet, its dependence graph consists of connected components which are complete.

In our interpretation of dependence/independence alphabets, fully-dependent alphabets will be employed for describing sequential systems while fully-independent alphabets represent concurrent systems which behave autonomously, i.e. without communication or interaction. Transitive alphabets may be employed for describing a set of sequential systems which do not communicate or interact.

Observing a (complex) system from an external viewpoint, one might see that the system is capable of executing actions. Furthermore, one might recognize dependencies of these actions. Thus, considering a single set of actions (together with an independence relation) is adequate for an external view onto a system whose internal structure is not known. However, if the internal structure of a system is given, a somehow different view might be more appropriate. We can understand the system as a set of n *processes* where each process i is capable of executing actions only among Σ_i. These actions are used for communication among the processes or correspond to internal actions of a process (cf. Chapter 2). This leads us to the notion of a *distributed alphabet*, a notion due to Zielonka:

Definition 3.1.5 ([Zie87])
A distributed alphabet $\tilde{\Sigma}$ *(over* n processes*) is an* n*-tuple* $(\Sigma_1, \ldots, \Sigma_n)$ *of (not necessarily disjoint) alphabets. For* $\tilde{\Sigma}$*, we let* $Proc(\tilde{\Sigma})$ *denote the set* $\{1, \ldots, n\}$.

When the distributed alphabet is clear from the context, we simplify our notation and write *Proc* instead of $Proc(\tilde{\Sigma})$. In our interpretation, an action a in $\Sigma_i \cap \Sigma_j$ for two different $i, j \in Proc$ may be employed for a communication of the different processes i and j.

To show a natural correspondence of distributed and independence alphabets, we introduce the operators *alphabet* and *pr*. Let us fix the distributed alphabet $\tilde{\Sigma} = (\Sigma_1, \ldots, \Sigma_n)$. Then we let $alphabet_{\tilde{\Sigma}}$ denote the set $\Sigma = \Sigma_1 \cup \cdots \cup \Sigma_n$. Furthermore, let the domain of $pr_{\tilde{\Sigma}}$ range over $alphabet_{\tilde{\Sigma}}$ and let $pr_{\tilde{\Sigma}}(a)$ yield the set $\{i \in Proc(\tilde{\Sigma}) \mid a \in \Sigma_i\}$. We now define an *independence relation* $\mathrm{I}(\tilde{\Sigma})$ by $\mathrm{I}(\tilde{\Sigma}) = \{(a, b) \in \Sigma \times \Sigma \mid pr(a) \cap pr(b) = \emptyset\}$. It is easy to see that $\mathrm{I}(\tilde{\Sigma})$ is well defined, i.e., it is indeed an independence relation. Hence, $(\Sigma, \mathrm{I}(\tilde{\Sigma}))$ is an independence alphabet.

On the other hand, for an independence alphabet (Σ, I), we get a unique (up to the order of the alphabets) distributed alphabet $\tilde{\Sigma}$ such that $(\Sigma, I) = (\Sigma, \mathrm{I}(\tilde{\Sigma}))$ by considering maximal (totally) dependent subsets of Σ, i.e., the subsets $\Sigma_i \subseteq \Sigma$ such that $(a, b) \notin I$ for all $a, b \in \Sigma_i$.

[3] A graph (V, E) is *complete* iff $E = (V \times V) \setminus \Delta(V)$.

Example 3.1.6 The independence alphabet of Example 3.1.4 corresponds to the distributed alphabet $(\{a, b\}, \{a, c\}, \{b, d\}, \{c, d\})$ and vice versa.

Note that the maximal dependent subsets of an independence alphabet correspond to the maximal D-cliques in its dependence graph representation. Recall that a set $p \subseteq \Sigma$ is called a *D-clique* iff $p \times p \subseteq D$.

Let us now extend the notion of independence and dependence to pairs of sets of actions. Given an independence alphabet (Σ, I) and two subsets $X, Y \subseteq \Sigma$ of actions, we say that X and Y are *independent* iff $X \times Y \subseteq I$. This is denoted by XIY. This means that any pair of actions from X and Y is independent with respect to (Σ, I). In a similar manner, we say that X and Y are *dependent*, iff there are two actions a and b in X respectively Y such that $(a, b) \in D$ where $D = \Sigma^2 \setminus I$. Hence, two sets are dependent iff two of their members are dependent. This will be denoted by XDY. If one of the sets X or Y is a singleton, we sometimes omit the curly braces to simplify our notation. For example, we will write aIY instead of $\{a\}IY$.

3.2 Traces

The behavior of systems may be described by the actions which are executed. The nature of a sequential system is that it can only execute one action after the other. Hence, an execution of a sequential system may be described by a sequence of actions which constitutes a linear order.

Given a concurrent system with a fixed notion of dependence, we will no longer expect the different actions of the execution to form a linear order but a partial order, as we pointed out in Chapter 2.

Therefore, we define an execution of a concurrent system to be a partial order.

Definition 3.2.1
Let Σ be an alphabet. A Σ-labeled partially ordered set *is a triple $(E, \leq, \lambda)$ such that*

- *$(E, \leq)$ is a partially ordered set (*poset*), i.e., $\leq \subseteq E \times E$ and $\leq$ is reflexive, transitive, and antisymmetric,*
- *λ is a* labeling function *from E to Σ which assigns to every element of E a* label *which is an element of Σ.*

If the alphabet Σ is clear from the context, we may omit it. The labeling function λ can be extended to subsets of E in a straightforward manner viz for $C \subseteq E$, we define $\lambda(C)$ to denote $\{\lambda(e) \mid e \in C\}$.

Example 3.2.2 Let us fix the alphabet $\Sigma = \{a, b, c, d\}$ in this example.